Phaltan Education Society's
College of Engineering, Phaltan
Department of Electronics and Telecommunication Engineering
Academic Year 2017-18

KURUKSHETRA 2017
EVENT 1: Circuit Design

Entry Fee: Rs. 100/- per Group.

Rules:

1. ID card is mandatory.
2. The size of the team should be two members.
3. Aptitude test will be for 30 marks and time duration is 30 min. Aptitude questions will be on circuit design. On the basis of marks candidate will be selected for next round.
4. Candidates will be provided with components related to circuit design.
5. On the basis of circuit execution with correct output, candidate will get selected for next round. Time duration for this round is 30 min.

Coordinator Name:
Prof. Nale S.B – 9106863036

Student Coordinator:
1. Monika Ashok Dhembre: 9689262795
2. Jyoti Subhash Dhumal: 8329475336
3. Rutuja Sanjay Kadam: 8329614572
Event 2: Best From Waste

Entry Fee: Rs. 100/- per Group.

Rules:

1. No. of Participant in team- 2 maximum.
2. Model should be made up from waste material and it is useful.
3. You can use e-waste or other waste (paper, bottle, metal pieces etc.)
4. College will not provide any material to make model.
5. Winner will be declared on the basis of innovative idea, model complexity and material used.

Coordinator Name:

Prof. Ms. Sonwalkar A.M. - 8830515659

Student Coordinator:

1. Dhanshree Yadav : 9834237989
2. Kiran Ingawale : 9730130083
Entry Fee: Rs. 100/- per Group.

Rules:
1. ID card is mandatory.
2. Maximum Two members are allowed in One Group.
3. Candidates should bring two hard copies and one soft copy of the paper at the time of presentation.
4. The presentation time is for 6 mins and queries for 2 mins for each paper.
5. The paper should be in IETE format (available on college website) and Paper should not exceed six pages.
6. Candidate should submit soft copy of paper (in pdf format) on email: coepkuru17@gmail.com
7. Bring your Soft Copies in CD or Pen drive.

Topics:
1. Nanotechnology in Electronics.
2. Artificial Intelligence
3. Wireless communication
4. Latest Technologies for Non-conventional sources of energy
5. Power electronics.
6. Embedded and VLSI system.

Coordinator Name:
Prof. S. P. Jagtap – 9673725363

Student Coordinator:
1. Amruta Newase: 8329459642
2. Amruta Unawane: 8208027265
Rules:-
1  Number of Participant= 2 (max)
2  Size of Poster= A2(11.7*16.5 inch)
3  For one entry only one poster is allowed.
4  The time duration is 10 minutes for explanation.
5  On basis of explanation and poster, winner will get selected.
6  Any material will not be provided by college.

Entry Fee: Per head 50/- RS. OR Per Group Rs. 100/-

Note: - Any topic containing message.

Coordinator Name:  Prof. M. M. Gargade (9970329286)

Student Coordinator:

1) Shivadas Shinde (9604770317)   Coordinator
2) Miss Ashwini Kumbhar (7083577219)  Member
3) Miss Pranali Salunkhe (8805043693)  Member
4) Miss Sneha Nimbalkar (8530916832)  Member
5) Miss Sujata Bondre (8805395145)  Member
EVENT 5: Model Making

Entry Fee: Rs. 100/- per Group.

Rules:

1. ID card is mandatory.
2. Maximum Four members are allowed in One Group.
3. The model should be built on the basis of digital electronics concept (combinational and sequential circuits).
4. The model should not be built by any electronics or electrical materials.
5. Candidate should explain the circuit with appropriate truth table and then demonstrate the model.
6. The model is expected to work for any input combination.

Coordinator Name:

Prof. S. R. Badiger - 8830515659

Student Coordinator:

1. Dhanashri Yadav 8482861663
2. Snehal Adsul 9921776257